ECSE 425. COMPUTER ARCHITECTURE.

Credits: 3

Offered by: Electrical & Computer Engr (Faculty of Engineering)

This course is not offered this catalogue year.

Description

Trends in technology. CISC vs. RISC architectures. Pipelining. Instruction level parallelism. Data and Control Hazards. Static prediction. Exceptions. Dependencies. Loop level paralleism. Dynamic scheduling, branch prediction. Branch target buffers. Superscalar and N-issue machines. VLIW. ILP techniques. Cache analysis and design. Interleaved and virtual memory. TLB translations and caches.

- (3-1-5)
- Prerequisites: ECSE 324
- Tutorials assigned by instructor.