

ECSE 541. DESIGN OF MULTIPROCESSOR SYSTEMS-ON-CHIP.

Credits: 3

Offered by: Electrical & Computer Engr (Faculty of Engineering)

Terms offered: Winter 2026

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Description

Modelling, design, evaluation, and optimization of multiprocessor systems-on-chips (MPSoCs). Introduction to system-level modelling of MPSoC architecture; system performance, power, and lifetime modelling; fault and defect tolerance; automatic general and heuristic design space exploration and design optimization; resource allocation, application mapping, and task scheduling.

- (3-0-6)
- Corequisites: ECSE 420 or ECSE 421 or ECSE 425 or ECSE 444

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